

**Amendments To The Specification:**

Please replace the Title with the following rewritten Title:

--SIGNAL PROCESSING SYSTEM FOR REDUCING POWER CONSUMPTION--

Please add the following new heading and paragraph after the Title on line 3 of page 1:

**--Related Applications**

This application is a divisional and claims priority to earlier filed U.S. Application Ser. No. 09/805,963, filed March 14, 2001.--

Please replace the Abstract on page 9, with the following rewritten Abstract:

--An apparatus is provided for generating a plurality of control clocks in response to a power state of a number of processing units that includes a first multiplexor for selectively outputting one of a DC level and a source clock in response to a power down signal, a branch for branching the output of the first multiplexor into a predetermined number of branches, and a number of multiplexors, each for selecting respective ones of the clock signals from the branch and the DC level in response to the power state of a corresponding one of the plurality of processing units and outputting the selected signal to the corresponding processing unit.--